FKeras: A Sensitivity Analysis Tool for Edge Neural Networks

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 - \circ Quantization
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When do hardware faults occur?

Example: LHC's CMS Data Processing Pipeline



Ref: https://indico.fnal.gov/event/46746/contributions/210450/attachments/141293/177902/hirschauer_AE_CPAD_19mar2020.pdf

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How does the ECON-T Autoencoder **tolerate** radiation?

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TMR incurs ≥200% area overhead!

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How can we **reduce** radiation tolerance **costs**?

Observation: Tolerance only applied to hardware

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What about **software**?

How should we assess the **fault sensitivity** of NN **software**?

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Bit-level fault injection with fine-grained control



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Small Pareto (Total Weight Bits: 10,240)









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• Conceptually, each fault injection campaign:

1. Generates X "faulty" variants by flipping a single weight bit 2. Measures the new EMD on a set of test inputs

3. Determines the weight bits to protect

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Can we **quantify** fault sensitivity a priori?

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Original Model

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Ranking Metric

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High \rightarrow Low Ranking

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 - Hessian (computed at weight level)

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FKeras: Future Work

- We want to use FKeras to:
 - Analyze more edge NNs and datasets
 - How much can our metrics speed up fault injection campaigns?
 - Perform NN design space exploration that considers fault sensitivity using our metrics
 - How does fault sensitivity interact with performance, area, etc?

Thank you! Questions?

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FKeras Repo: https://github.com/KastnerRG/fkeras

Backup

How do our metrics compare with a perfect ranking?

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How to use bit-level sensitivity rankings?

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• Speed up fault injection campaigns

How to use bit-level sensitivity rankings?

• Speed up fault injection campaigns

• Design space exploration